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## APPLICANTS

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This application is a DIV of 09/808,114 03/15/2001 PAT 6,734,510

\*\* FOREIGN APPLICATIONS \*\*\*\*\* *JMJ**NONE*

## IF REQUIRED, FOREIGN FILING LICENSE GRANTED

\*\* 06/14/2004

Foreign Priority claimed	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no	STATE OR COUNTRY OR	SHEETS DRAWING 10	TOTAL CLAIMS 24	INDEPENDENT CLAIMS 3
35 USC 119 (a-d) conditions met	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after				
Verified and Acknowledged	<i>Sonjae M. Hanas JMJ</i> Allowance Examiner's Signature Initials				

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## TITLE

Technique to mitigate short channel effects with vertical gate transistor with different gate materials

FILING FEE	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees ( Filing ) <input type="checkbox"/> 1.17 Fees ( Processing Ext. of time ) <input type="checkbox"/> 1.18 Fees ( Issue )
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